



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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2/3/03

Applicants: Tuan Q. Dao, Pius Ng and Paul Look § GROUP ART UNIT: 2181
§
Serial No.: 09/343,872 § EXAMINER: Kim T. HUYNH
§
Filed: June 30, 1999 § Attorney Docket No. 5201-20400
§
For: Modular and Scalable System Bus § LSI Docket No. 99-038
Structure §

AMENDMENT; RESPONSE TO OFFICE ACTION OF SEPTEMBER 24, 2002

Commissioner for Patents
Washington, D.C. 20231

RECEIVED January 21, 2003

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Technology Center 2100

Dear Sir:

This paper is submitted in response to the above named office action to further highlight why the application is in condition for allowance.

AMENDMENT

Please amend the case as follows:

In the Claims:

Please cancel claim 9.

Please amend the claims to read as follows. The changes to amended claims are shown in the subsequent section.

1. (Once Amended) A data transfer apparatus that comprises:

a plurality of busses each configured to couple a processing device to a corresponding memory module;

at least one cross-bus coupled to each of the plurality of busses by one or more bus bridges, wherein the bus bridges each include a set of multiplexers that are configurable to steer signals from the bus to the cross-bus, and are further configurable to steer signals from the cross-bus to the bus; and